# Jay Greco Electrical Engineer <u>jay-greco.com</u> | <u>github.com/jaygreco</u>

3755 Birchwood Dr.(720) 341-6273Boulder, CO 80304Jonathan.Greco@Colorado.edu

I'm an electrical engineer with a knack for thinking big. I am curious, motivated, and strive to learn and improve. I love to dive deep into difficult problems, work hard, and collaborate with an interdisciplinary team to develop cutting-edge products.

# **Recent Professional Experience**

Tensentric Electrical Engineer

- Electrical hardware lead, *Project Azure*: Lab automation device design for the largest scientific instrument company in the world.
  - Interfacing with software, mechanical, and management to deliver a successful product to customers

July 2015 - present

- Driving system and low-level design, system bring-up, integrating, testing, documenting, and delivering complex electrical systems at the prototype and production level
- Project deliverables include schematics, PCB layouts, cable designs and drawings, finalized components and subassemblies, and documented engineering and real-world testing
- Formal documentation and verification testing: Responsible for requirements and test protocol creation, test system design, and formal execution of software/hardware verification & validation (V&V) for multiple projects.
  - o Included hardware fixtures & related test systems, tool documentation, and formal tool validations

# Other Professional Experience

National Instruments Electrical Engineering Intern	Summer 2014
Emerson Process Management Engineering Intern	Summer 2013
Colorado Power Electronics Center (CoPEC) Undergraduate Researcher	2013 - 2015

# Other Relevant Skills and Experience

- Following established best practices and guidelines such as coding conventions, project templates, and electrical DFx (Design for Reliability, Manufacturing)
- Programming and scripting in C/C++, C#, Python, MATLAB
- Solidworks CAD and additive manufacturing (FDM, SLA)
- Completion of designs using Mentor Graphics, Cadence Allegro, EAGLE, Altium Designer
- Power electronics theory, design and multiple completed power electronics design
- Experience working with prototyping, manufacturing, and assembly houses for PCB procurement
- Comfortable quickly iterating on prototype hardware: design, procure, bring-up, test, spin, and repeat
- Growth mindset, excellent problem-solving and communication skills, a good attitude, and high standards

#### **Education**

#### University of Colorado Bachelors of Science, Electrical Engineering

- GPA 3.96/4.0, Summa Cum Laude
- Power Electronics & Control Systems tracks, certificate in Engineering Leadership
- Extensive undergraduate lab study under Profs. Dragan Maksimovic and Robert Erickson

#### Extracurricular Activities

- Design, build, test, & flight of multiple civilian small unmanned aerial systems (sUAS)
- Hardware hacking, teardowns and reverse engineering, including a popular blog post on reverse engineering the Amazon Dash Button (see <u>blog.jay-greco.com</u>)
- Consumer multirotor design, including a fully open-source flight controller with integrated power delivery system and on-screen display (see <u>jay-greco.com/openaio</u>)



References, Detailed Project Descriptions, and Design Files & Sample Work Readily Available Upon Request